## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Hiroshi HASHIMOTO et al.

Group Art Unit: 2814

Serial Number: 10/083,533

Examiner: Thao X. Le

Filed: February 27, 2002

Confirmation Number: 6400

For:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING A SEMICONDUCTOR DEVICE HAVING A STABLE THRESHOLD

**CHARACTERISTIC** 

Attorney Docket Number:

020244

Customer Number:

38834

## PETITION FOR EXTENSION OF TIME

Mail Stop: RCE

Commissioner for Patents

October 14, 2005

P. O. Box 1450

Alexandria, VA 22313-1450

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated June 15, 2005 for One (1) month from September 15, 2005 to October 15, 2005.

Attached please find a check in the amount of \$120.00 for the cost of the extension. If any additional fees are due in connection with this paper, please charge our Deposit Account No. 50-2866.

Respectfully submitted,

WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP

00000089 10083533 10/17/2005 JADDO1

120.00 OP 02 FC:1251

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MJC/asc